

RTEST 2015

The CSI Symposium on

Real-Time and Embedded Systems and Technologies (RTEST)

RTEST is an annual symposium targeted at bringing researchers in the fields of real-time systems, embedded systems, and related technologies, from both industry and academia together. This year the event is planned to be held at 7-8 October 2015 in Sharif University of Technology, Tehran, Iran. This event covers five main areas, including: real-time software and applications and related tools, embedded hardware, real-time and embedded networks, real-time and embedded operating systems, and system-level design.

RTEST 2015 considers qualified theoretical and practical research papers in the abovementioned areas reporting design, analysis, implementation, evaluation, and empirical results. There also will be a work-in-progress (WiP) session to discuss novel research/practical ongoing activities with the experts in the field. The most qualified papers will be invited to extend their contributions to be reviewed for possible publication in two journals.

Main Areas:

Submitted papers must be in the scope of the conference, namely in the field of **Real-Time and Embedded Systems and Technologies**. RTEST 2015 covers the following topics:

- 1. Real-Time Software and Applications, and Related Tools:** Software architecture, programming languages, compiler and development environments, WCET estimation, software analysis and design (and the respective toolset), real-time simulation, test and verification, model verification and validation, different application areas like communications, medical, intelligent transportation, intelligent buildings, multimedia systems, aerospace, ...
- 2. Embedded Hardware:** Multicore and manycore systems, power and energy consumption, energy harvesting, battery management, processor temperature, fault-tolerance, reliability, security, embedded architectures, resource management
- 3. Real-Time and Embedded Networks:** Network scheduling, network protocols, tools for modeling, design and analysis of embedded networks, wireless sensor networks, cyber-physical systems, network control systems
- 4. Real-Time and Embedded Operating Systems:** Open-source operating systems and standards, OS architecture, drivers, security, fault-tolerance, reliability, power consumption, scheduling algorithms, resource management, performance, test and debug, runtime environment, middleware, multiprocessor systems (**Note:** papers for this track must include real implementations and enough justification for practicability. In special cases, simulation may also be acceptable.)
- 5. System-Level Design:** This track includes contributions which combines ideas of different levels of system design including tracks 1 to 4 (e.g., hardware/software codesign or cross-layer optimization).

Submission of Papers

Submissions must be in PDF format and must be submitted online via the RTEST 2015 EasyChair conference management system. The material must be unpublished and not under submission elsewhere. Submissions must not exceed 8 pages long (2 columns, 10 pt) for the main text, inclusive of figures, tables, and references. All papers submitted in *English* must conform, at the time of submission, to the [IEEE Formatting Guidelines](#). Submissions in *Persian* should follow the [CSI formatting guidelines](#). Papers exceeding the page limit will not be reviewed. Note that the submission deadline is a firm deadline and will not be extended.

The accepted English papers will be indexed by IEEE Xplore, and accessible through the IEEE Xplore digital library. A selection of the best papers will receive outstanding paper awards, and will be highlighted as such in the conference proceedings. Also, these papers will be invited to be extended for publication in selected journals after another round of review.

Important Dates:

Full paper submission: ~~June 22, 2015~~ **July 6, 2015** (Firm Deadline)

Acceptance notification: August 06, 2015

Camera-ready: August 20, 2015

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Alireza Ejlali (Sharif University of Technology)

Mehdi Kargahi (University of Tehran)

Seyed Ghassem Miremadi (Sharif University of Technology)

Ali Movaghar (Sharif University of Technology)

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